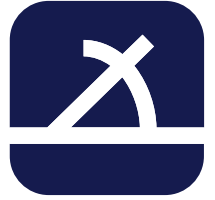


Prover Extractor



Prover Extractor™ provides automated extraction of logic from relay schematics. It has been used for many years in formal verification and simulation of Relay-Based Interlocking (RBI) systems.

Benefits

Increased Quality. Prover Extractor provides checks of schematics, so that you can automatically detect deviations from your guidelines.

Enable Simulation and Formal Verification. By extracting the logic from relay schematics, you can apply independent tools that perform various tasks using this logic, such as simulation or formal verification.

Cost and Time Savings. By finding errors early in the process, you avoid project delays and extra costs in the end. On-site testing will be substantially quicker when fewer errors are found.

Key Features

Guideline checks. See directly on the screen where guidelines are violated.

Logic Extraction. Run Prover Extractor on a set of drawings to extract the logic.

Navigation. Turn your set of drawings into a hyperspace where you easily navigate between drawings.

Interfaces

Prover Extractor is a desktop tool for Windows.

Prover Extractor supports Bentley MicroStation and Autodesk AutoCAD schematics.

Prover Extractor can interpret logic and write logic formulae to text files for further processing.

Supported Platforms

Microsoft Windows 11, not older than 3 years.

Support for additional Windows platforms can be purchased.

Expert Services

Expert services can be provided, including training, analysis as a service, and advice about best practices. Contact Prover for a quotation.

Get Started with Prover Extractor

Scan the QR code or visit www.prover.com/products/prover-extractor/ to learn more, watch a demo, or get in touch with our experts.

